



2A Low Dropout Regulator with Enable

Features

- Adjustable Output from 1.2V to 4.8V Using External Resistors
- 1.5V, 1.8V and 2.5V options by Setting ADJ Pin Below 0.2V
- Fixed 2.5V, 3.3V and 5V Output for TO-252
- Over current and over temperature protection
- 500mV dropout @2A
- Enable pin
- 10µA quiescent current in shutdown
- Output recovery mode in OTP
- Connect ADJ to GND for fixed output mode
- TO-252 and TO-252-5 Package

Applications

- Battery powered systems
- Motherboards
- Peripheral cards
- Set Top Boxes
- Notebook Computers

Ordering Information

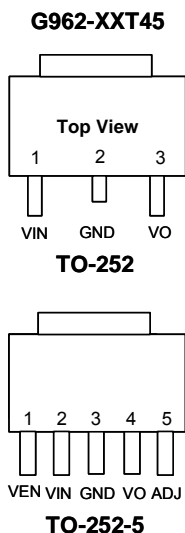
ORDER NUMBER	ORDER NUMBER (Pb free)	MARKING	TEMP. RANGE	PACKAGE
G962-25T45U	G962-25T45Uf	G962-25	-40°C ~ +85°C	TO-252
G962-33T45U	G962-33T45Uf	G962-33	-40°C ~ +85°C	TO-252
G962-50T45U	G962-50T45Uf	G962-50	-40°C ~ +85°C	TO-252
G962-15ADJTJU	G962-15ADJTJUf	G962-15	-40°C ~ +85°C	TO-252-5
G962-18ADJTJU	G962-18ADJTJUf	G962-18	-40°C ~ +85°C	TO-252-5
G962-25ADJTJU	G962-25ADJTJUf	G962-25	-40°C ~ +85°C	TO-252-5

Note: T4: TO-252 TJ: TO-252-5

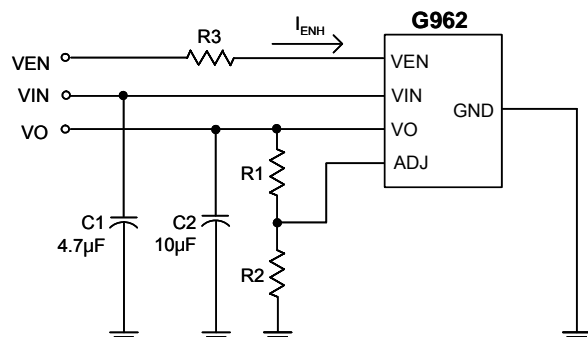
U : Tape & Reel

e.g. 18 denotes the 1.8V output voltage.

Pin Configuration



Typical Application Circuit



$$VO = \frac{1.2 (R1+R2)}{R2} \text{ Volts}$$

R2=12kΩ is recommended

R3 should be connected for current I_{ENH} restriction as $V_{EN} > V_{IN}+0.3V$



Absolute Maximum Ratings	(Note 1)
Input Voltage	7V
V _{EN} Voltage	V _{IN} +0.3V
Power Dissipation Internally Limited	(Note 2)
Maximum Junction Temperature	150°C
Storage Temperature Range	-65°C ≤ T _J ≤ +150°C
Reflow Temperature (Soldering, 10 sec)	260°C
Thermal Resistance Junction to Ambient, (θ _{JA})	
TO-252, TO-252-5	95°C/W
Thermal Resistance Junction to Case, (θ _{JC})	
TO-252, TO-252-5	8°C/W
ESD Rating (Human Body Model)	2kV

Operation Conditions	(Note 1)
Input Voltage	2.2V ~7V
Temperature Range	-40°C ≤ T _A ≤ +85°C

Electrical Characteristics

V_{EN}=V_{IN}, V_{IN}=5V, I_O=0.5A, C_{IN}=4.7μF, C_{OUT}=10μF, T_A=T_J=25°C unless otherwise specified (Note 3)

PARAMETER	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT
Output Voltage	V _O	V _{IN} =V _O +0.7V, I _O =10mA	-2	V _O	2	%
Line Regulation		V _O +0.7V ≤ V _{IN} ≤ 5.5V, I _O =10mA	---	0.2	2	%
Load Regulation		10mA ≤ I _O ≤ 2A	---	0.8	2	%
Quiescent Current	I _Q	V _O =3.3V, V _{EN} =V _{IN}	---	3	3.5	mA
		V _{EN} =0V	---	16	35	μA
Ripple Rejection		f _i =120Hz, 1V _{P-P} , I _O =100mA	---	55	---	dB
Dropout Voltage	V _D	G962-15, I _{OUT} =2A	---	0.9	1.1	V
		G962-18, I _{OUT} =2A	---	0.7	0.9	
		G962-25, I _{OUT} =2A	---	0.6	0.7	
		G962-33, I _{OUT} =2A	---	0.5	0.6	
		G962-50, I _{OUT} =2A, V _{IN} =6V	---	0.45	0.55	
Short Circuit Current			---	0.8	---	A
Over Temperature		(Note 4)	---	150	---	°C
V _{EN} Voltage High	V _{ENH}	Output Active	1.6	---	---	V
V _{EN} Voltage Low	V _{ENL}	Output Disabled	---	---	0.4	V
V _{EN} Bias Current Low	I _{ENL}	V _{EN} =0.4V	---	---	20	μA
ADJ Reference Voltage	V _{REF}	V _{IN} =2.2V, V _{ADJ} =V _{OUT} , I _O =10mA	1.176	1.2	1.224	V
ADJ Pin Threshold			---	0.2	---	V

Note 1: Absolute Maximum Ratings are limits beyond which damage to the device may occur. Operating Conditions are conditions under which the device functions but the specifications might not be guaranteed. For guaranteed specifications and test conditions see the Electrical Characteristics.

Note2: The maximum power dissipation is a function of the maximum junction temperature, T_{Jmax}; total thermal resistance, θ_{JA}, and ambient temperature T_A. The maximum allowable power dissipation at any ambient temperature is (T_{Jmax}-T_A)/θ_{JA}. If this dissipation is exceeded, the die temperature will rise above 150°C and IC will go into thermal shutdown.

Note3: Low duty pulse techniques are used during test to maintain junction temperature as close to ambient as possible.

Note4: The over temperature point is guarantee by design.

Definitions

Dropout Voltage

The input/output voltage differential at which the regulator output no longer maintains regulation against further reductions in input voltage. Measured when the output drops 2% below its nominal value, dropout voltage is affected by junction temperature, load current and minimum input supply requirements.

Line Regulation

The change in output voltage for a change in input voltage. The measurement is made under conditions of low dissipation or by using pulse techniques such that average chip temperature is not significantly affected.

Load Regulation

The change in output voltage for a change in load

current at constant chip temperature. The measurement is made under conditions of low dissipation or by using pulse techniques such that average chip temperature is not significantly affected.

Maximum Power Dissipation

The maximum total device dissipation for which the regulator will operate within specifications.

Quiescent Bias Current

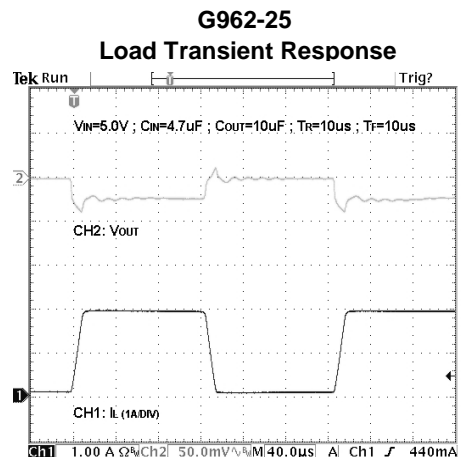
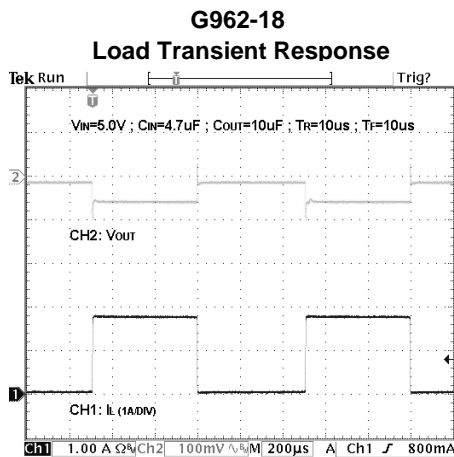
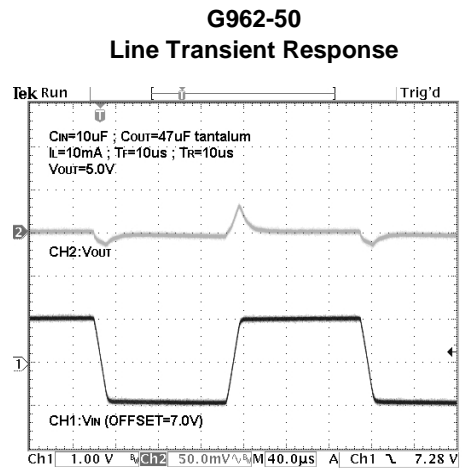
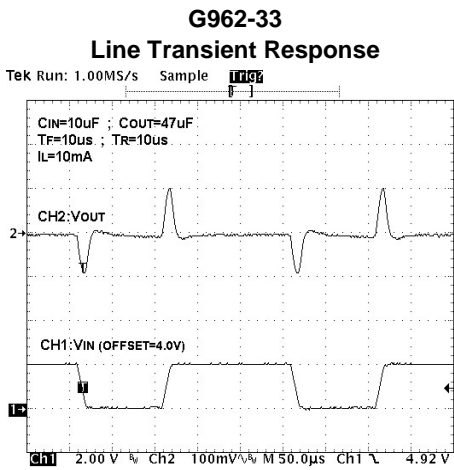
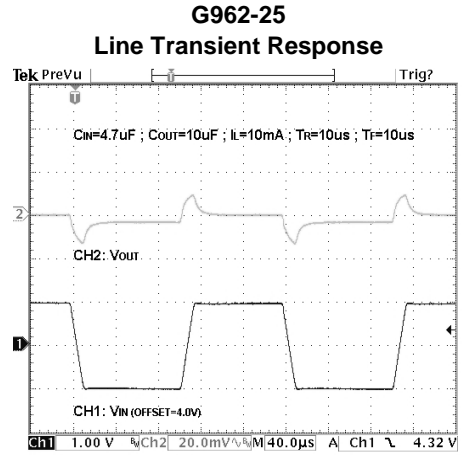
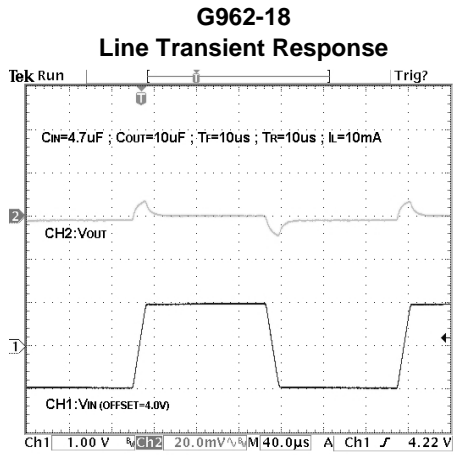
Current which is used to operate the regulator chip and is not delivered to the load.

Output Capacitor for Stable Condition

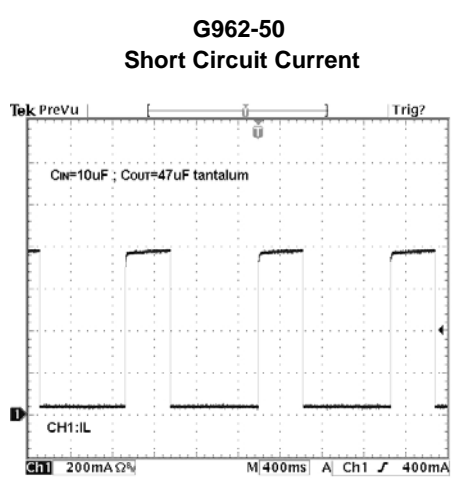
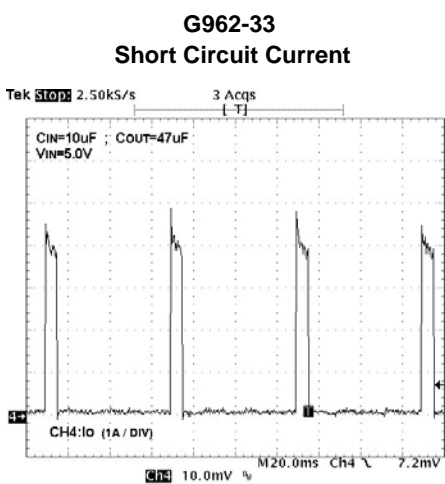
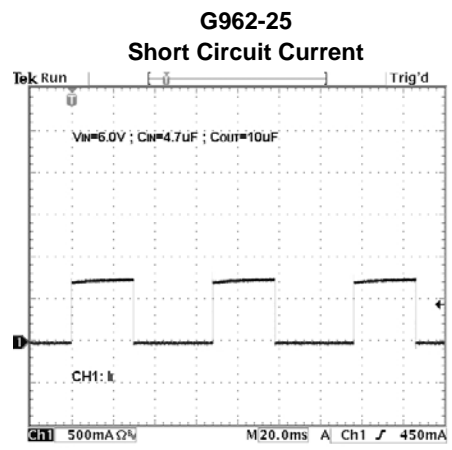
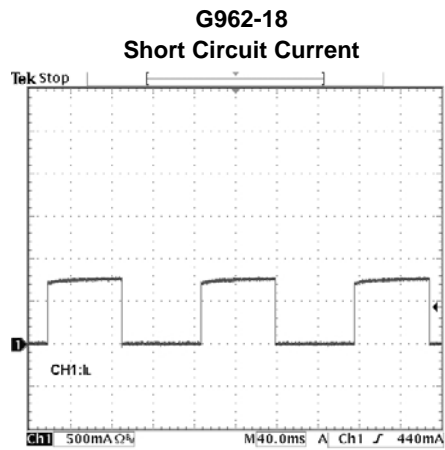
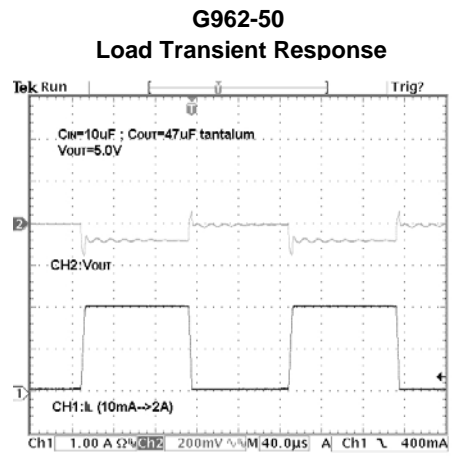
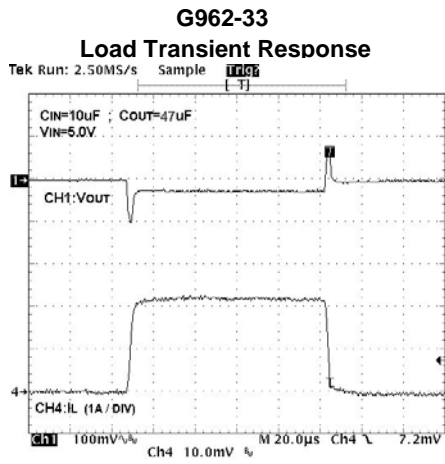
The G962 can be stable for X5R MLCC capacitor larger than 10μF or POSCAP capacitor larger than 47μF.

Typical Characteristics

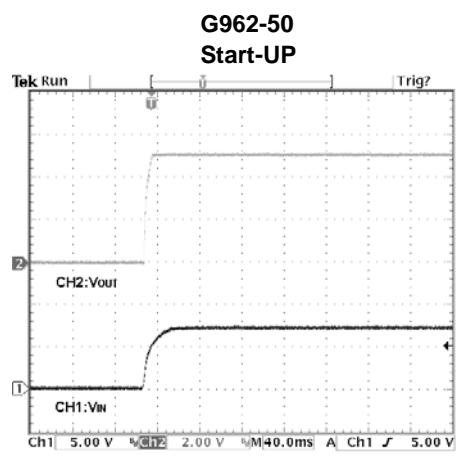
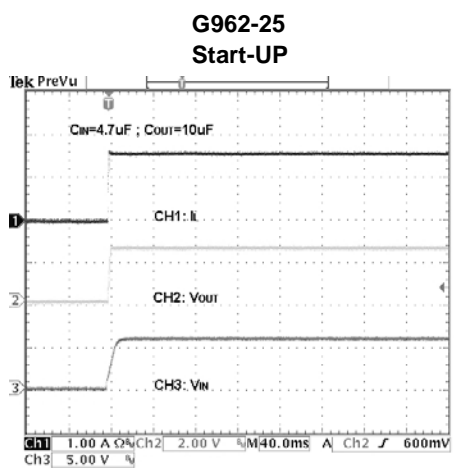
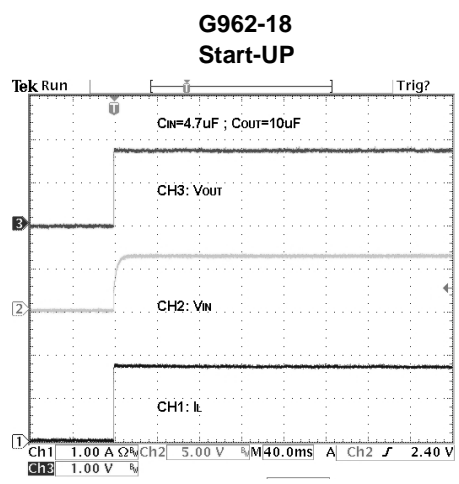
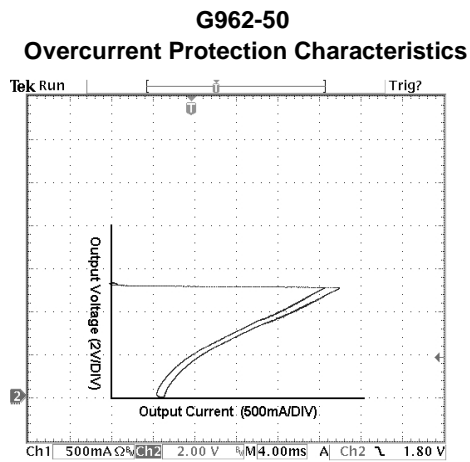
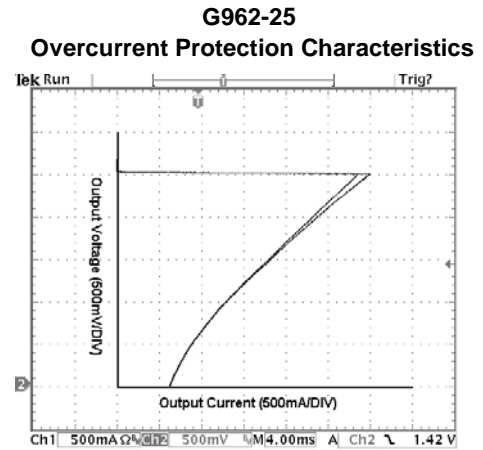
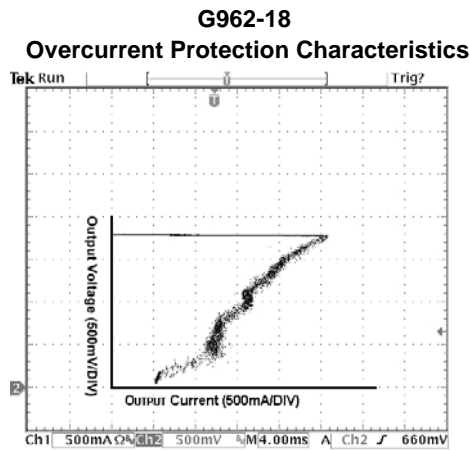
$V_{EN}=V_{IN}$, $=5V$, $I_O = 0.5A$, $C_{IN} = 4.7\mu F$, $C_{OUT} = 10\mu F$, $T_A = T_J = 25^\circ C$



Typical Characteristics (continued)

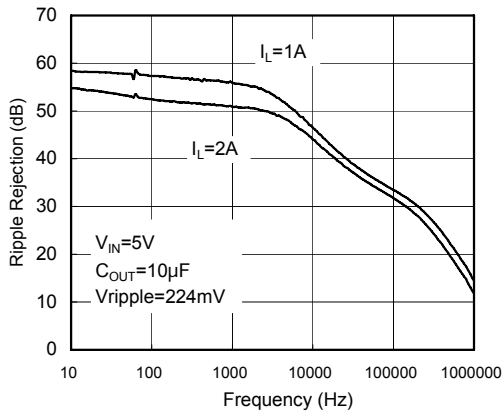


Typical Characteristics (continued)

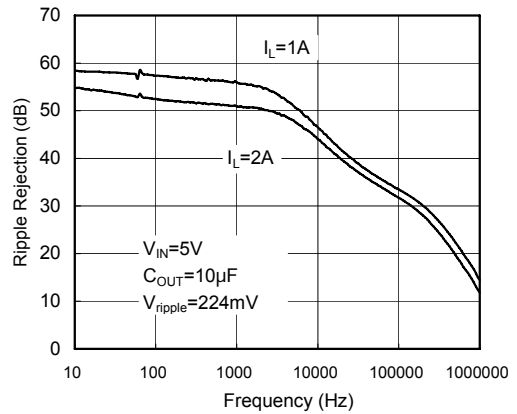


Typical Characteristics (continued)

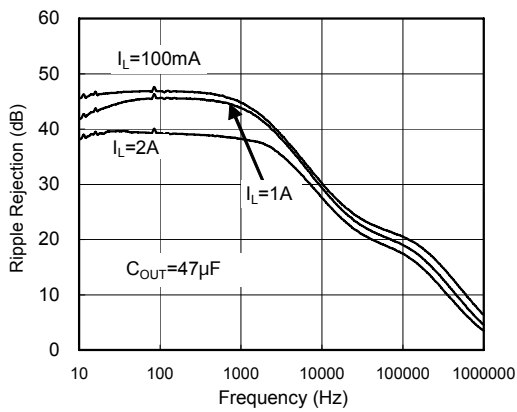
G962-18
Ripple Rejection



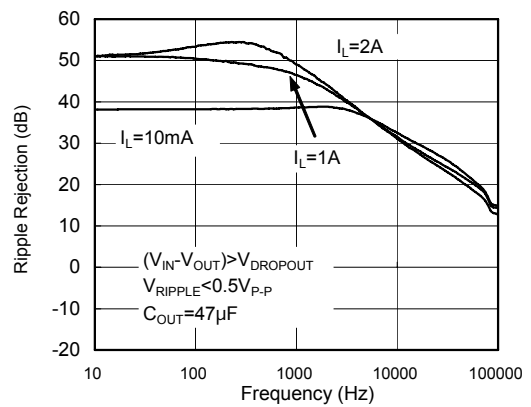
G962-25
Ripple Rejection



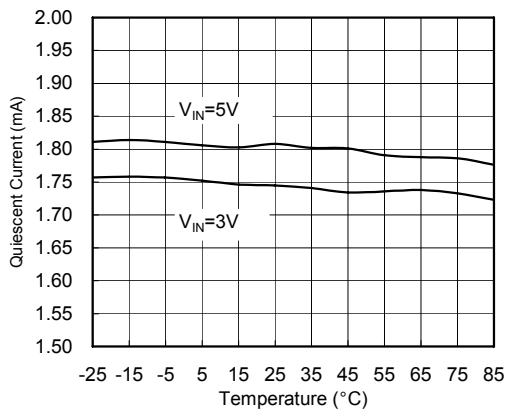
G962-33
Ripple Rejection



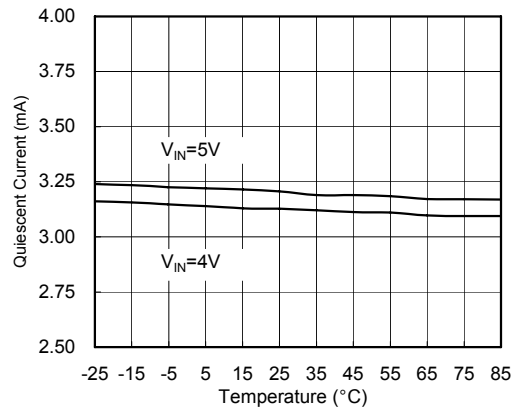
G962-50
Ripple Rejection



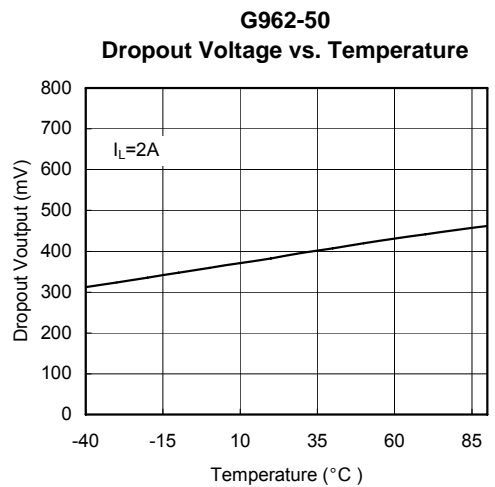
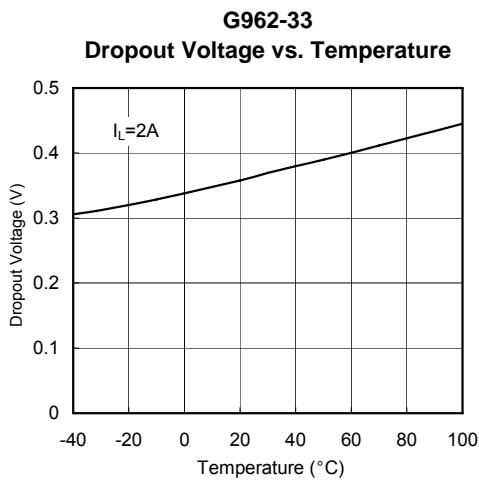
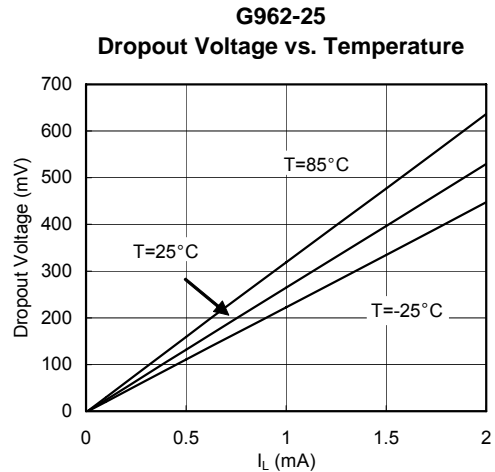
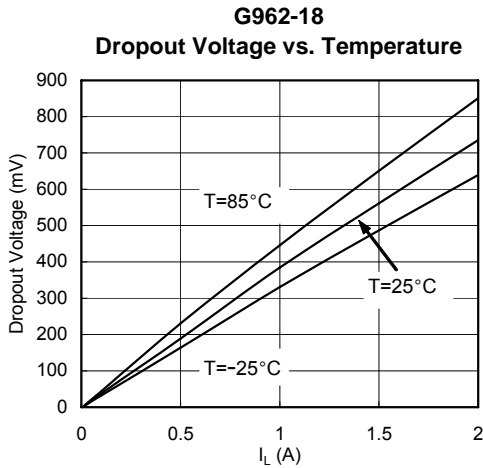
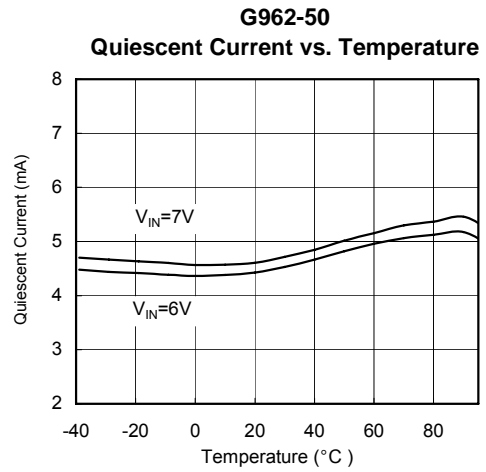
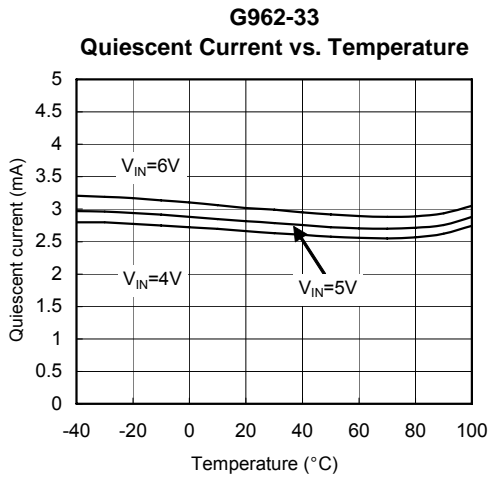
G962-18
Quiescent Current vs. Temperature



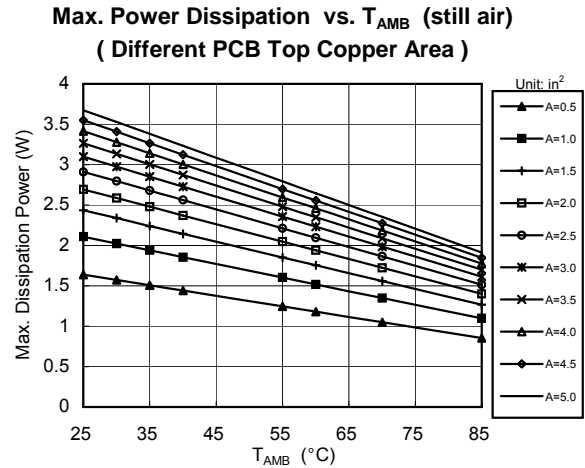
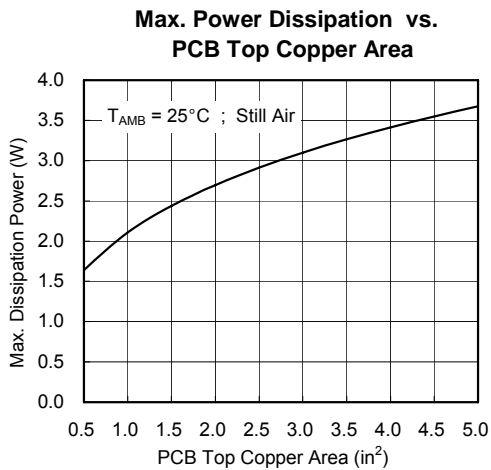
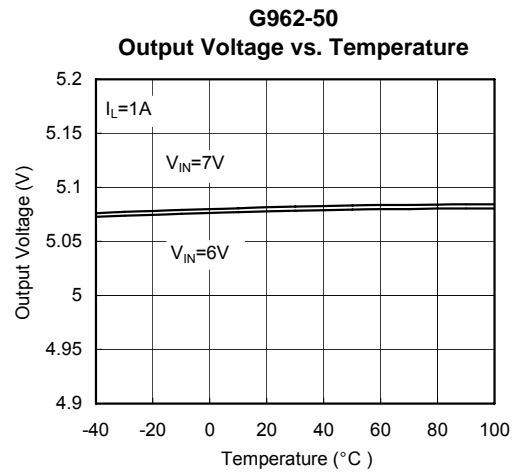
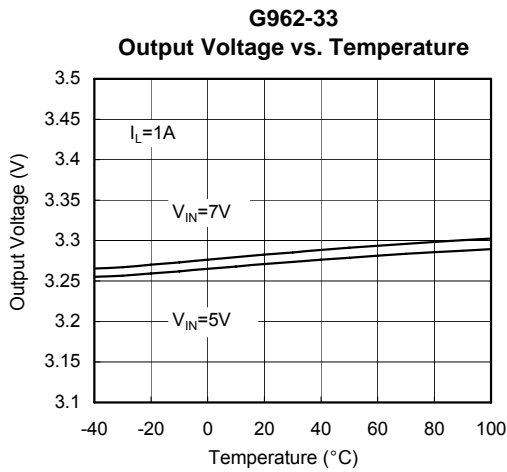
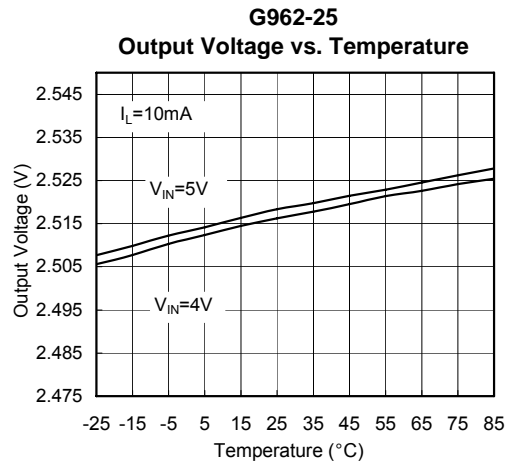
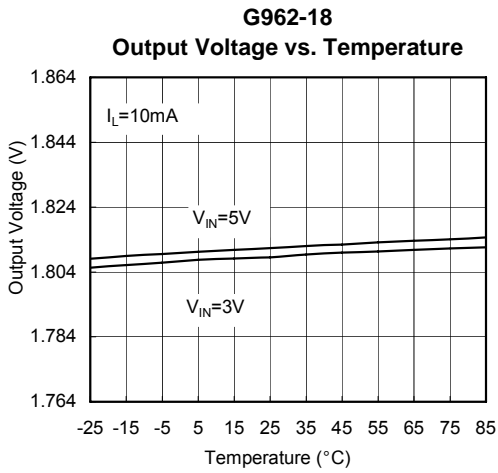
G962-25
Quiescent Current vs. Temperature



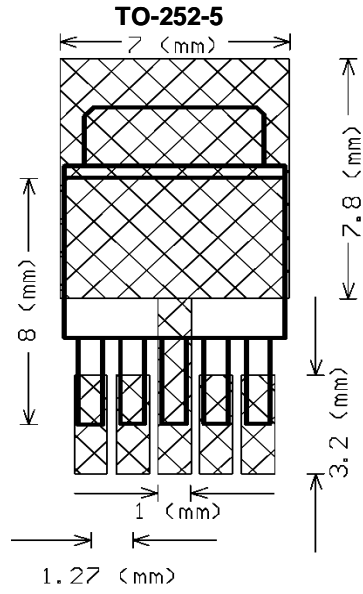
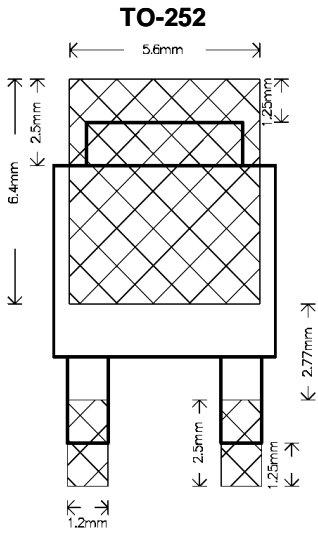
Typical Characteristics (continued)



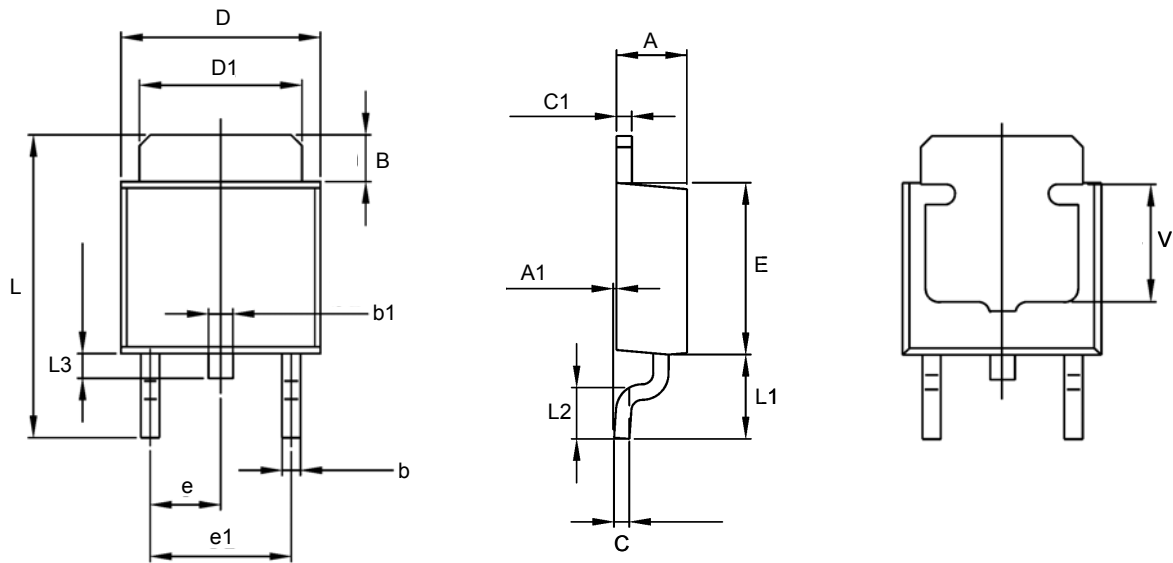
Typical Characteristics (continued)



Recommend Minimum Footprint

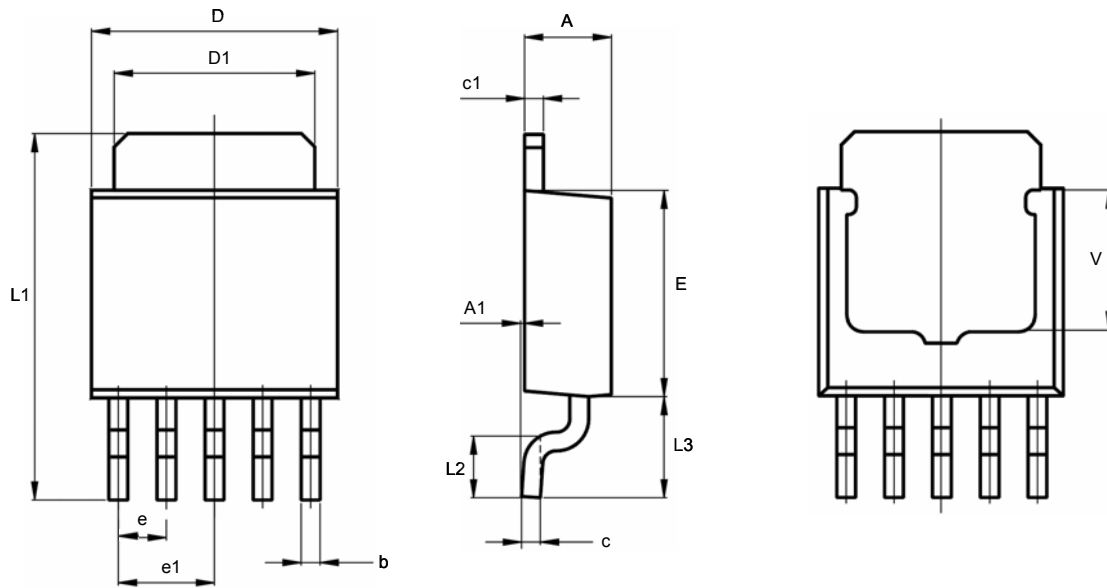


Package Information



TO-252 (T4) Package

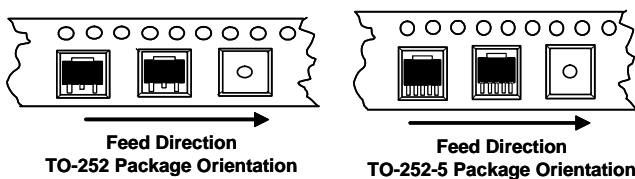
SYMBOL	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	2.190	2.400	0.086	0.094
A1	0.000	0.127	0.000	0.005
B	0.880	1.650	0.035	0.065
b	0.500	0.880	0.020	0.035
b1	0.700	0.900	0.028	0.035
C	0.430	0.580	0.017	0.023
C1	0.430	0.580	0.017	0.023
D	6.350	6.730	0.250	0.265
D1	5.200	5.460	0.205	0.215
E	5.400	6.220	0.213	0.245
e	2.300 TYP		0.091 TYP	
e1	4.500	4.700	0.177	0.185
L	9.400	10.42	0.370	0.410
L1	2.550	2.900	0.100	0.114
L2	1.400	1.780	0.055	0.070
L3	0.350	1.020	0.014	0.040
V	4.320 REF		0.170 REF	



TO-252-5 (TJ) Package

SYMBOL	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	2.190	2.400	0.086	0.094
A1	0.000	0.127	0.000	0.005
b	0.400	0.880	0.016	0.035
c	0.430	0.580	0.017	0.023
c1	0.430	0.580	0.017	0.023
D	6.350	6.730	0.250	0.265
D1	5.200	5.460	0.205	0.215
E	5.400	6.220	0.213	0.245
e	1.270 TYP		0.050 TYP	
e1	2.540 TYP		1.000 TYP	
L1	9.400	9.900	0.370	0.410
L2	1.400	1.780	0.055	0.070
L3	2.550	2.900	0.100	0.114
V	3.800 REF		0.150 REF	

Taping Specification



PACKAGE	Q'TY/REEL
TO-252	2,500 ea
TO-252-5	2,500 ea

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